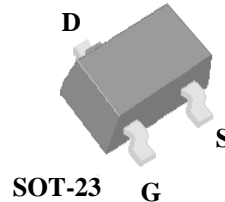




- ▼ Simple Drive Requirement
- ▼ Small Package Outline
- ▼ Surface Mount Device

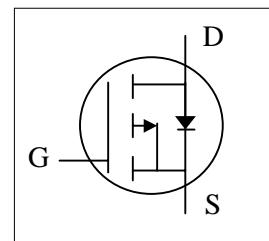


BV_{DSS}	-60V
$R_{DS(ON)}$	250m Ω
I_D	- 1.8A

Description

Advanced Power MOSFETs from APEC provide the designer with the best combination of fast switching, low on-resistance and cost-effectiveness.

The SOT-23 package is widely preferred for all commercial-industrial surface mount applications and suited for low voltage applications such as DC/DC converters.



Absolute Maximum Ratings

Symbol	Parameter	Rating	Units
V_{DS}	Drain-Source Voltage	- 60	V
V_{GS}	Gate-Source Voltage	± 20	V
$I_D@T_A=25^\circ C$	Continuous Drain Current ³	- 1.8	A
$I_D@T_A=70^\circ C$	Continuous Drain Current ³	- 1.4	A
I_{DM}	Pulsed Drain Current ¹	-10	A
$P_D@T_A=25^\circ C$	Total Power Dissipation	1.38	W
	Linear Derating Factor	0.01	W/ $^\circ C$
T_{STG}	Storage Temperature Range	-55 to 150	$^\circ C$
T_J	Operating Junction Temperature Range	-55 to 150	$^\circ C$

Thermal Data

Symbol	Parameter	Value	Unit
Rthj-a	Maximum Thermal Resistance, Junction-ambient ³	90	$^\circ C/W$



AP2311GN

Electrical Characteristics @ $T_j=25^\circ\text{C}$ (unless otherwise specified)

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Units
BV_{DSS}	Drain-Source Breakdown Voltage	$V_{GS}=0V, I_D=-250\mu A$	-60	-	-	V
$\Delta BV_{DSS}/\Delta T_j$	Breakdown Voltage Temperature Coefficient	Reference to 25°C , $I_D=-1\text{mA}$	-	-0.04	-	$V/^\circ\text{C}$
$R_{DS(ON)}$	Static Drain-Source On-Resistance ²	$V_{GS}=-10V, I_D=-1.8A$	-	200	250	m Ω
		$V_{GS}=-4.5V, I_D=-1.4A$	-	240	300	m Ω
$V_{GS(th)}$	Gate Threshold Voltage	$V_{DS}=V_{GS}, I_D=-250\mu A$	-1	-	-3	V
g_{fs}	Forward Transconductance	$V_{DS}=-10V, I_D=-1A$	-	2	-	S
I_{DSS}	Drain-Source Leakage Current	$V_{DS}=-60V, V_{GS}=0V$	-	-	-10	μA
	Drain-Source Leakage Current ($T_j=70^\circ\text{C}$)	$V_{DS}=-48V, V_{GS}=0V$	-	-	-25	μA
I_{GSS}	Gate-Source Leakage	$V_{GS}=\pm 20V$	-	-	± 100	nA
Q_g	Total Gate Charge ²	$I_D=-1A$	-	6	10	nC
Q_{gs}	Gate-Source Charge	$V_{DS}=-48V$	-	1	-	nC
Q_{gd}	Gate-Drain ("Miller") Charge	$V_{GS}=-4.5V$	-	3	-	nC
$t_{d(on)}$	Turn-on Delay Time ²	$V_{DS}=-30V$	-	8	-	ns
t_r	Rise Time	$I_D=-1A$	-	5	-	ns
$t_{d(off)}$	Turn-off Delay Time	$R_G=3.3\Omega, V_{GS}=-10V$	-	22	-	ns
t_f	Fall Time	$R_D=30\Omega$	-	3	-	ns
C_{iss}	Input Capacitance	$V_{GS}=0V$	-	510	810	pF
C_{oss}	Output Capacitance	$V_{DS}=-25V$	-	50	-	pF
C_{rss}	Reverse Transfer Capacitance	$f=1.0\text{MHz}$	-	40	-	pF
R_g	Gate Resistance	$f=1.0\text{MHz}$	-	6.4	9.6	Ω

Source-Drain Diode

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Units
V_{SD}	Forward On Voltage ²	$I_S=-1.2A, V_{GS}=0V$	-	-	-1.2	V
t_{rr}	Reverse Recovery Time ²	$I_S=-1A, V_{GS}=0V,$	-	30	-	ns
Q_{rr}	Reverse Recovery Charge	$di/dt=100A/\mu s$	-	38	-	nC

Notes:

1. Pulse width limited by Max. junction temperature.
2. Pulse width $\leq 300\mu s$, duty cycle $\leq 2\%$.
3. Surface mounted on 1 in² copper pad of FR4 board, $t \leq 10\text{sec}$; $270^\circ\text{C}/W$ when mounted on Min. copper pad.

THIS PRODUCT IS SENSITIVE TO ELECTROSTATIC DISCHARGE, PLEASE HANDLE WITH CAUTION.

USE OF THIS PRODUCT AS A CRITICAL COMPONENT IN LIFE SUPPORT OR OTHER SIMILAR SYSTEMS IS NOT AUTHORIZED.

APEC DOES NOT ASSUME ANY LIABILITY ARISING OUT OF THE APPLICATION OR USE OF ANY PRODUCT OR CIRCUIT DESCRIBED HEREIN; NEITHER DOES IT CONVEY ANY LICENSE UNDER ITS PATENT RIGHTS, NOR THE RIGHTS OF OTHERS.

APEC RESERVES THE RIGHT TO MAKE CHANGES WITHOUT FURTHER NOTICE TO ANY PRODUCTS HEREIN TO IMPROVE RELIABILITY, FUNCTION OR DESIGN.

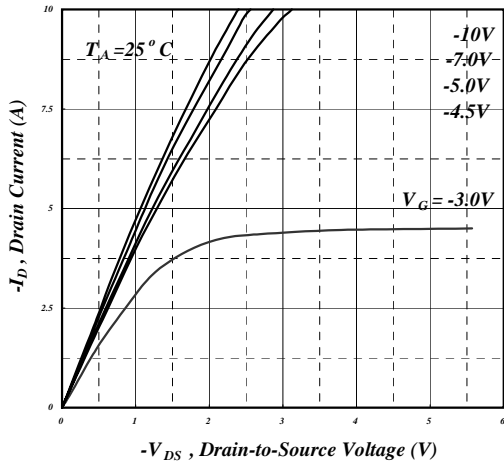


Fig 1. Typical Output Characteristics

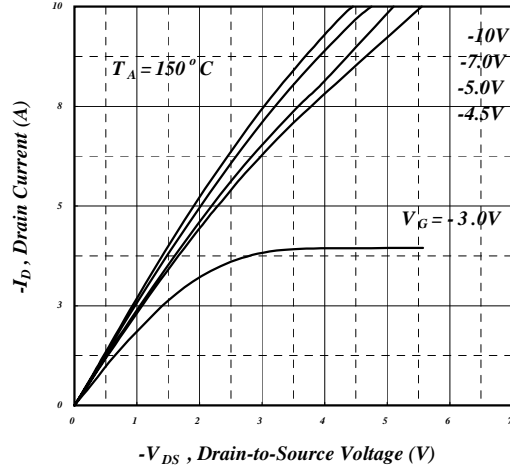


Fig 2. Typical Output Characteristics

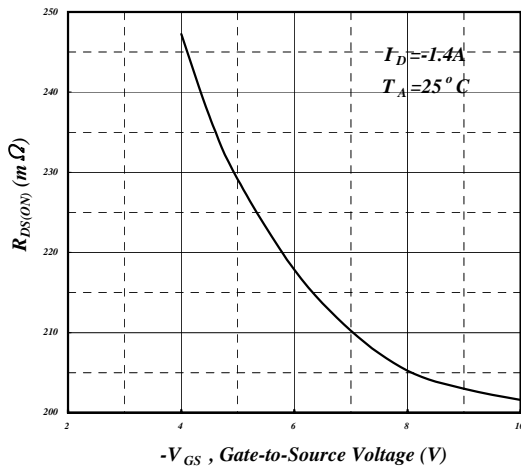


Fig 3. On-Resistance v.s. Gate Voltage

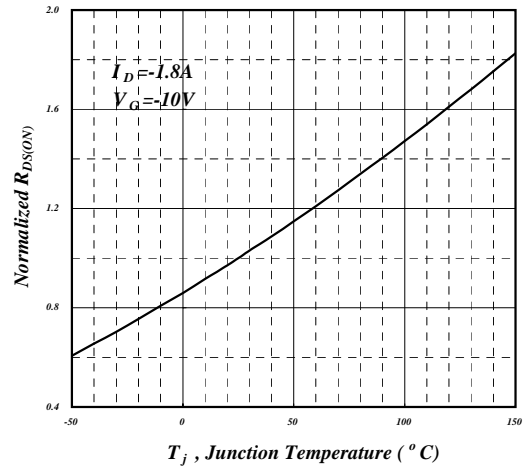


Fig 4. Normalized On-Resistance v.s. Junction Temperature

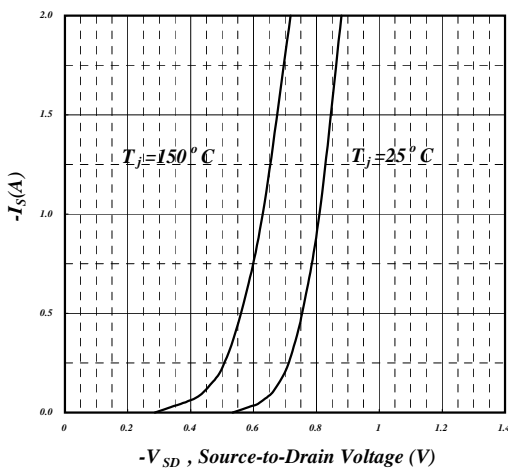


Fig 5. Forward Characteristic of

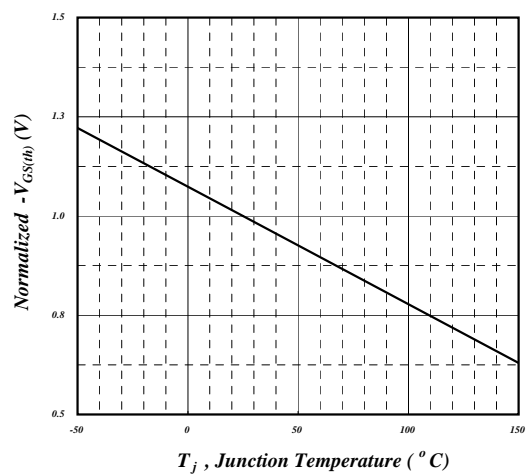


Fig 6. Gate Threshold Voltage v.s.

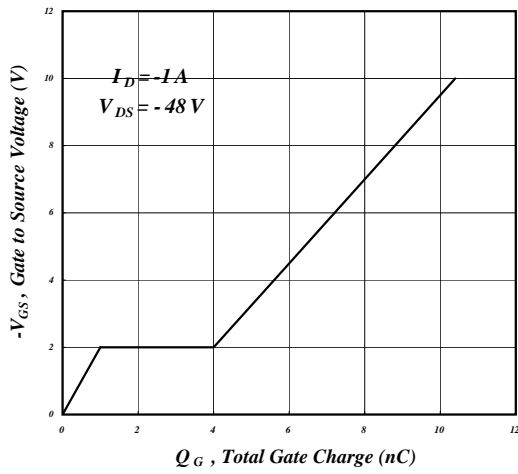


Fig 7. Gate Charge Characteristics

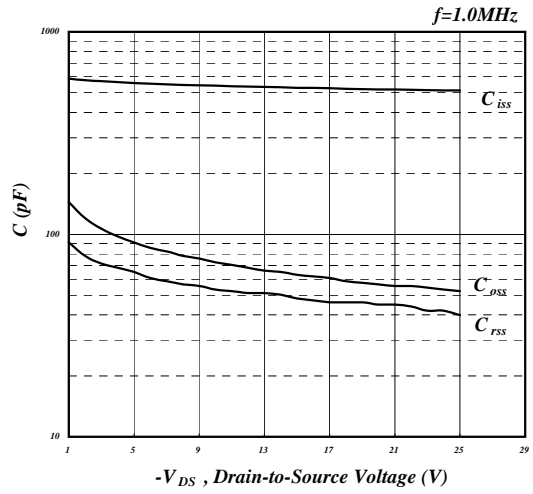


Fig 8. Typical Capacitance Characteristics

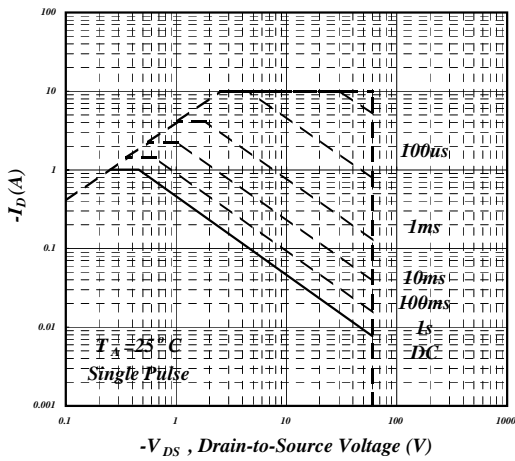


Fig 9. Maximum Safe Operating Area

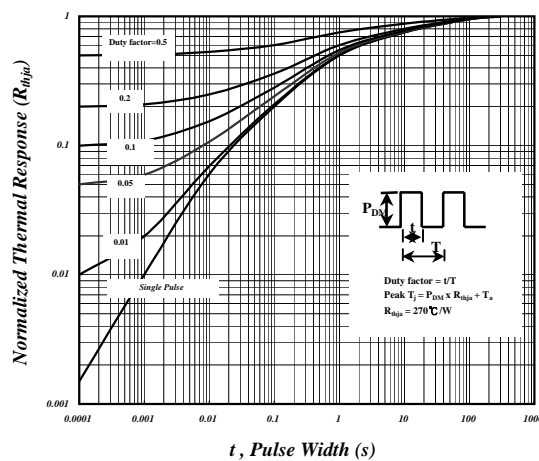


Fig 10. Effective Transient Thermal Impedance

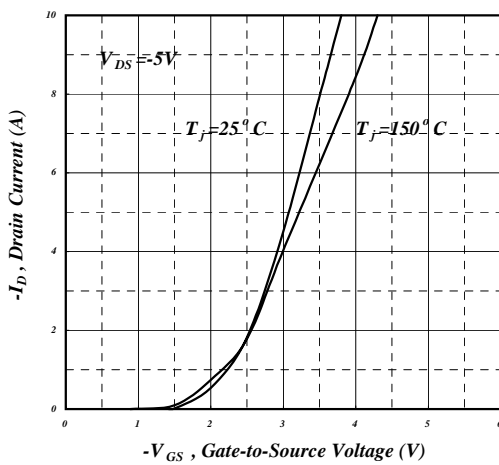


Fig 11. Transfer Characteristics

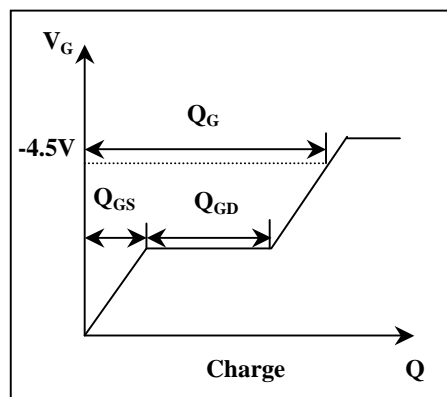
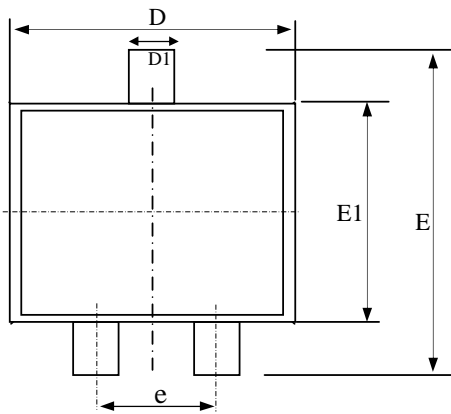


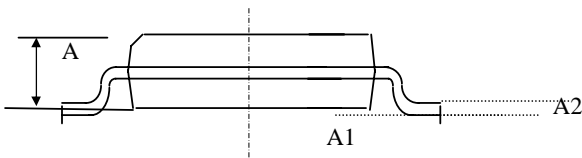
Fig 12. Gate Charge Circuit



Package Outline : SOT-23



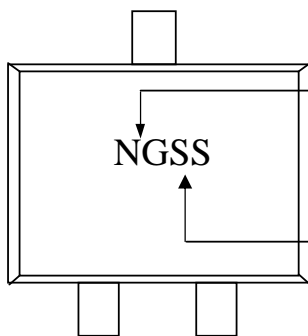
SYMBOLS	Millimeters		
	MIN	NOM	MAX
A	1.00	1.15	1.30
A1	0.00	--	0.10
A2	0.10	0.15	0.25
D1	0.30	0.40	0.50
e	1.70	2.00	2.30
D	2.70	2.90	3.10
E	2.40	2.65	3.00
E1	1.40	1.50	1.60



- 1.All Dimension Are In Millimeters.
- 2.Dimension Does Not Include Mold Protrusions.

Part Marking Information & Packing : SOT-23

Laser Marking



Part Number : NG

Date Code :

SS:2004,2008,2012...

SS:2003,2007,2011...

SS:2002,2006,2010...

SS:2001,2005,2009...

"A~Z" showed on 3rd position --> week 1 ~ week 26,

"A~Z" showed on 4th position --> week 27 ~ week 52.